

DETAILED ACTION

Claim Rejections - 35 USC § 112

The 112 rejection under 112 first paragraph in the final office action dated 3/31/09 has been withdrawn.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 6-26 are rejected under 35 U.S.C. 102(b) as being anticipated by Prabhu (6,791,072).

3. Regarding claim 6, Prabhu (e.g. figure 7c) teaches an integrated circuit arrangement comprising a nonplanar substrate 701 in which an integrated circuit 702 is formed on a side of the substrate arranged on a carrier 703 wherein the carrier is produced from a chemically resistant material (column 5 line 65-column 6 line 2);(applicant asserts that ceramic is “highly chemically resistant”).

4. Regarding claim 7, Prabhu teaches wherein the carrier is produced from a ceramic (column 5 line 65-column 6 line 2).

5. Regarding claim 8, Prabhu teaches that the side of the carrier which is remote from the integrated circuit has a planar surface (see figure 7c).

6. Regarding claim 9, Prabhu teaches that the side of the substrate that is connected to the carrier is entirely connected to the carrier (701 and 703).

7. Regarding claim 10, Prabhu teaches that the carrier has a cavity in which the substrate is completely held 703.
8. Regarding claim 11, Prabhu teaches that the carrier has a cavity in which the substrate is completely held 703.
9. Regarding claim 12, Prabhu teaches that the side of the substrate that is connected to the carrier is entirely connected to the carrier (701 and 703).
10. Regarding claim 13, Prabhu teaches that the carrier has a cavity in which the substrate is completely held 703.
11. Regarding claim 14, Prabhu teaches that the carrier has a cavity in which the substrate is completely held 703.
12. Regarding claim 15, Prabhu teaches that the side of the carrier which is remote from the integrated circuit has a planar surface (see figure 7c).
13. Regarding claim 16, Prabhu teaches that the side of the substrate that is connected to the carrier is entirely connected to the carrier (701 and 703).
14. Regarding claim 17, Prabhu teaches that the carrier has a cavity in which the substrate is completely held 703.
15. Regarding claim 18, Prabhu teaches that the carrier has a cavity in which the substrate is completely held 703.
16. Regarding claim 19, Prabhu teaches that the side of the substrate that is connected to the carrier is entirely connected to the carrier (701 and 703).
17. Regarding claim 20, Prabhu teaches that the carrier has a cavity in which the substrate is completely held 703.

Art Unit: 2826

18. Regarding claim 21, Prabhu teaches that the carrier has a cavity in which the substrate is completely held 703.

19. Regarding claim 22, Prabhu (e.g. figure 7c) teaches an integrated circuit arrangement comprising a curved substrate 701, having an integrated circuit 702 formed in the outer side of the substrate, where the substrate is arranged on a carrier 703, wherein the carrier is produced from a chemically resistant material (column 5 line 65-column 6 line 2);(applicant asserts that ceramic is “highly chemically resistant”).

20. Regarding claim 23, Prabhu teaches wherein the carrier is produced from a ceramic (column 5 line 65-column 6 line 2).

21. Regarding claim 24, Prabhu teaches that the side of the carrier which is remote from the integrated circuit has a planar surface (see figure 7c).

22. Regarding claim 25, Prabhu teaches that the side of the substrate that is connected to the carrier is entirely connected to the carrier (701 and 703).

23. Regarding claim 26, Prabhu teaches that the carrier has a cavity in which the substrate is completely held 703.

Response to Arguments

24. Applicant's arguments with respect to claims 6-26 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

25. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP

Art Unit: 2826

§ 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to KRISTA SODERHOLM whose telephone number is (571)272-8344. The examiner can normally be reached on M and Th 6:30-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sue Purvis can be reached on 571-272-1236. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2826

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/KZS/

/Sue A. Purvis/

Supervisory Patent Examiner, Art Unit 2826